

Abstract

A flash memory is provided with a protect area PA in which reading of a specific block is prohibited. A RAM to be used as a work area of a program is provided with a protect area PA1 in which reading of a specific block is prohibited. A bus state controller 3 compares a value of a program counter and a value of an address signal to prohibit reading in areas other than the protect area PA in the flash memory and to prohibit reading of data of protect area PA1 in the RAM other than reading from the protect area PA of the flash memory. For instance, when a user reads data of the protect area PA from a user access area to which the user can access, senseless data such as H'FFFF are output from the bus state controller 3 via a data bus.